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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/732,979	12/11/2003	Axel Brintzinger	2002 P 09238 US 8578			
48154 73	590 12/12/2006		EXAMINER			
SLATER & N		NGUYEN, DILINH P				
17950 PRESTON ROAD SUITE 1000			ART UNIT	PAPER NUMBER		
DALLAS, TX	DALLAS, TX 75252			2814		

DATE MAILED: 12/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Applica	ation No.	Applicant(s)				
Office Action Summary		,979	BRINTZINGER, AXEL				
		er	Art Unit				
	DiLinh I		2814				
The MAILING DATE of this comm		<u> </u>		Iress			
Period for Reply	••		•				
A SHORTENED STATUTORY PERIOD WHICHEVER IS LONGER, FROM THE - Extensions of time may be available under the provisi after SIX (6) MONTHS from the mailing date of this co - If NO period for reply is specified above, the maximum - Failure to reply within the set or extended period for re Any reply received by the Office later than three mont earned patent term adjustment. See 37 CFR 1.704(b)	MAILING DATE OF ons of 37 CFR 1.136(a). In no immunication. In statutory period will apply and apply will, by statute, cause the ans after the mailing date of this	THIS COMMUNICATION event, however, may a reply be tind will expire SIX (6) MONTHS from application to become ABANDONE	N. nely filed the mailing date of this cor D (35 U.S.C. § 133).				
Status							
 Responsive to communication(s) This action is FINAL. Since this application is in condition closed in accordance with the practice. 	2b)⊠ This action is on for allowance exce	non-final. pt for formal matters, pro		merits is			
Disposition of Claims							
4) ⊠ Claim(s) <u>1-5,8-10,28 and 31-34</u> is 4a) Of the above claim(s) is 5) ⊠ Claim(s) <u>1-5,8-10 and 34</u> is/are al 6) ⊠ Claim(s) <u>28 and 31-33</u> is/are reject 7) ☐ Claim(s) is/are objected to 8) ☐ Claim(s) are subject to res	s/are withdrawn from olowed. cted.	consideration.					
Application Papers							
9) The specification is objected to by			_				
<i>'</i> — • · · · · · · · · · · · · · · · · · ·	10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any ol Replacement drawing sheet(s) include	= :			D 4 404(d)			
11) The oath or declaration is objected	-						
Priority under 35 U.S.C. § 119	•						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review 3) Information Disclosure Statement(s) (PTO/SB/0 Paper No(s)/Mail Date		4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate				

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 28 and 31-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Higashiguchi et al. (U.S. Pat. 5828128) in view of Joshi et al. (U.S. Pat. 6731003).

Higashiguchi et al. disclose an electronic component comprising:

a wafer 20;

a plurality of bond pads 24-26 disposed on the wafer;

a plurality of functional 3-D structures 22-23 disposed on the wafer 20, each functional 3-D structure including a compliant base element and having a first height; and

a plurality of other 3-D structures 21 disposed on the wafer to provide a mechanical reinforcement, each of the other 3-D structures having a second height that is greater than the first height (figs. 3 and 5, column 7, lines 56-67).

Higashiguchi et al. do not disclose a plurality of reroute traces, each reroute trace being electrically connected to one of the bond pad and extending onto a surface of one of the functional 3D structure.

However, Joshi et al. disclose an electronic component comprising a functional 3-D structure and a plurality of reroute traces 24 and 26, each reroute trace being

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electrically connected to one of the bond pad 32 extending onto a surface of one of the functional 3-D structure (fig. 4, column 3, lines 45-52). Therefore, it would have been obvious to one having ordinary in the art at the time the invention was made to modify the device structure of Higashiguchi et al. by having a plurality of reroute traces, each reroute trace being electrically connected to one of the bond pad and extending onto a surface of one of the functional 3D structure because as taught by Joshi et al., such the plurality of reroute traces would provide higher resistance and increase likelihood for the semiconductor package (column 6, lines 4-8).

- Regarding claim 31, Higashiguchi et al. disclose that the plurality of other 3-D
 structures have a greater height than the functional 3-D structures (figs. 3 and 5);
 therefore, the plurality of other 3-D structures would have a lower degree of
 compressibility than the functional 3-D structures.
- Regarding claim 32, Higashiguchi et al. disclose that the other 3-D structures 21
 are arranged in a regularly distributed manner in an edge region of the wafer 20
 (fig. 3).
- Regarding claim 33, Higashiguchi et al. disclose that the other 3-D structures 21
 are arranged in a regularly distributed manner over the wafer 20 (figs. 3 and 5).

Claims Allowed

Claims 1-5, 8-10 and 34 are allowed.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DLN

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